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APPROVAL SHEET

WLPN303010 Series SMD Molded Power Choke Inductors

*Contents in this sheet are subject to change without prior notice.

Approval sheet

Features

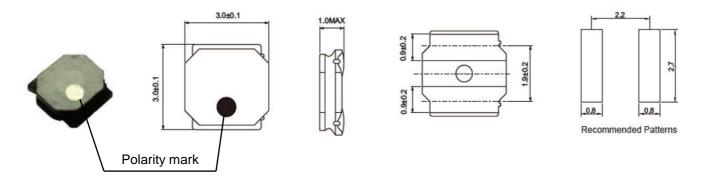
- 1. Close magnetic loop with magnetic resin shielded.
- 2. Low profile, High inductance.

Applications

- 1. General propose power inductor in DC power system.
- 2. Inductor in DC/DC converter.
- 3. Low profile for portable and wearable device.
- 4. LC filter in Audio D class Amplifier

Shape and Dimension

Unit: mm



Ordering Information

WL	PN	3030	10	Ν	1R2	Р	В
Product Code	Series	Dimensions	Thickness	Tolerance	Value	Packing Code	
WL: Inductor	Shielded SMD Power Inductors	3.0 * 3.0 mm	1.0 mm	M: ± 20% N: ± 30%	1R2 = 1.2uH 100 = 10uH	P=7" Reeled (Embossed Tape)	B:STD

Electrical Characteristics

			Test Freq (KHz)			Rated Current		
WLPN303010	L (uH)	Inductance Tolerance		DCR	SRF	(mA) Max		
Series				(Ω ± 20%)	(MHz)Min	Saturation Current Idc1	Temperature Rise Current Idc2	
WLPN303010N1R2PB	1.2	N	100	0.065	120	1700	1480	
WLPN303010N1R5PB	1.5	N	100	0.075	99	1440	1370	
WLPN303010M2R2PB	2.2	М	100	0.083	86	1300	1300	
WLPN303010M3R3PB	3.3	М	100	0.130	64	1000	1030	
WLPN303010M4R7PB	4.7	М	100	0.170	50	850	900	
WLPN303010M6R8PB	6.8	М	100	0.250	44	700	745	
WLPN303010M100PB	10	М	100	0.350	34	600	620	
WLPN303010M150PB	15	М	100	0.550	25	450	480	
WLPN303010M220PB	22	М	100	0.770	22	380	410	

1. Test Frequency: 100KHz.

2. Test Equipment:

Inductance: Chroma3302+1320+16502 or equivalent. DCR: Chroma16502 or equivalent. SRF: HP4291B or equivalent.

3. Saturation Current Idc1: The value of current causes a 30% inductance reduction from initial value.

4. Temperature rise current ldc2: The value of current causes a 40 $^\circ\! \mathbb C$ temperature rise.

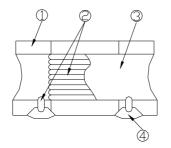
5. Rated Current: Either Idc1 or Idc2 whichever is smaller.

6. Operating Temperature Range:-25 $^\circ\!\mathrm{C}$ to +120 $^\circ\!\mathrm{C}$ (Including self-temperature rise).

7. Storage Temp. Range : -40 $^\circ\!\mathrm{C}$ to +85 $^\circ\!\mathrm{C}$.

8. MSL : Level 1.

Structural Drawing

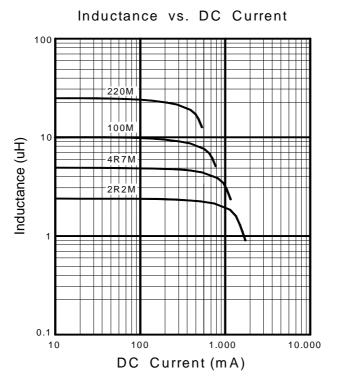


- ① Ferrite core : Ni-Zn ferrite.
- ② Winding wire : Polyurethane-copper wire.
- ③ Over-coating resin : Epoxy resin, containing ferrite powder.
- ④ Electrode : External electrode (substrate) Cu.

External electrode (top surface solder coating) Sn-Ag-Cu.

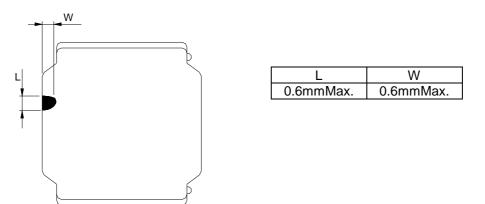


Characteristic Curve



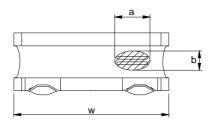
Core Chipping:

The appearance standard of the chipping size in top side, of bottom side ferrite core is following dimension.



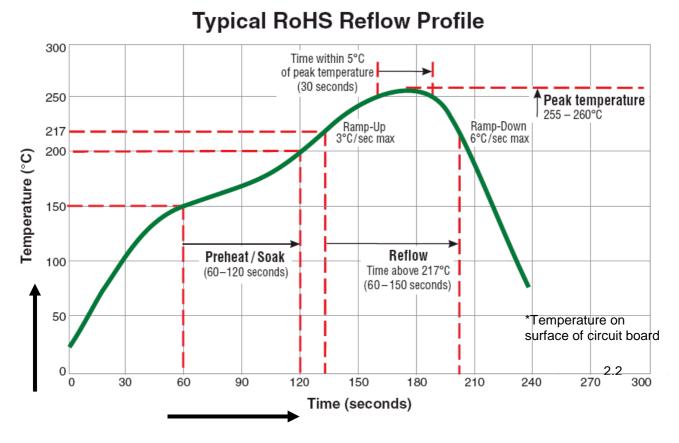


Exposed wire tolerance limit of coating resin part on product side Size of exposed wire occurring to coating resin is specified below.



- ① Width direction (dimension a): Acceptable when a<=w/2 Nonconforming when a>w/2
- ² Length direction (dimension b): Dimension b is not specified.
- ^③ When total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, that is acceptable.

Reflow Profile Chart (Reference):



(Table 1)

The products may be exposed to reflow soldering process of above profile up to two times.

Mechanical Performance /Environmental Test Performance Specifications: (WLPN303010 series)

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No.	ltem	Test condition	Requirements					
1	Resistance to Deflection.	No damage.	The test samples shall be soldered to the test board by the reflow soldering conditions show in Table 1. As illustrated below, apply force in the direction of the Arrow indicating until deflection of the test board Reaches to 2 mm. $R5 \xrightarrow{10}_{\text{Force Rod}} \underbrace{5.1}_{\text{Sample}} \underbrace{5.1}_{\text{Sammle}} \underbrace{5.1}_{\text{Sammle}} \underbrace{5.1}_{\text{Sammle}} \underbrace{5.1}_{\text{Sammle}} \underbrace$					
			Land dimensions Test board size :100×40×10 Unit: mm Test board material I: glass epoxy-resin Solder cream thickness:0.1					
2	Adhesion of Terminal Electrode.	Shall not come off PC board.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.					
3	Body strength.	No damage.	Applied force :20 N. Duration :10 s.					
4	Resistance to Vibration.	△L/L:within±10% No abnormality observed In appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.Then It shall be submitted to below test conditions.Frequency range10Hz~55HzTotal Amplitude1.5mm(May not exceed acceleration 196 m/S2)Sweeping Method10Hz to 55Hz to 10 Hz for 1 min.TimeFor 2 hours on each X, Y, and Z axis.					
5	Resistance to Soldering heat (Reflow).	△L/L:within±10% No abnormality observed In appearance	The test sample shall be exposed to reflow oven at 230±5 deg C for 40 seconds, with peak temperature at 260±5 deg C for 5 seconds, 2 times. Test board thickness: 1.0 mm. Test board material: Glass epoxy-resin.					

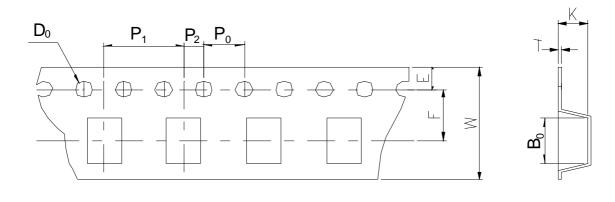


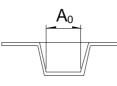
	Solder ability.	At least 90% of surface of terminal electrode is	molten s	t samples shall solder as shown	in below	table.	hen Immers	ed in		
		covered by new		ethanol solution r Temperature						
6		solder.	Solue	Time	245±deg C 5±1.0 S.					
			Immersing Speed		25 mm/s					
					2	5 1111/5				
7	Temperature Characteristics.	△L/L:within±20% No abnormality observed in appearance.	Measurement of inductance shall be taken at temperature range within -25 deg C to +85 deg C. With reference to inductance value at +20 deg C, change rate shall be calculated.							
	Thermal shock.	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed at specified shown in below table in sequence. The temperature cycle shall be repeated 100 cycles.							
8			Conditio	ns of steps for	1 cycle.					
			Step	Temperat		Time(r				
			1	-40±3 de	g C	30±	3			
			2	Room Te	mp	3 maxir	mum			
				3 85±2 deg C		30±				
	-		4			3 maxir				
9	Low Temperature life Test.	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. After that, the test samples shall be placed at test conditions as shown in below table. Temperature -40±2 deg C							
				Time	500 +	24/-0 h				
10	Loading at high temperature life test.	△L/L:within±10% No abnormality observed in appearance.	soldering The test	samples shall I g conditions sha samples shall I ture and applied ble.	own in Tab be placed	ole 1. in thermostat	ic oven set a	at specified		
10			Те	mperature	85±2	2 deg C				
			Арр	lied current		d current to Page 2)				
				Time		-24/-0 h				
11	Damp heat life test.	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.The test samples shall be placed in thermostatic oven set at specifie temperature and humidity as shown in below table.Temperature 60±2 deg CHumidity90~95%RH							
			•	Time		-24/-0 h				
	Loading under	\triangle L/L:within±10%	The test	samples shall l			board by the	reflow		
12	Damp heat life test.	No abnormality observed in appearance.	soldering conditions shown in Table 1.The test samples shall be placed in thermostatic oven set at specified temperature and humidity and applied the rated current continuously as shown in below table.Temperature60±2 deg C							
			Humidity 90~95%RH							
			aqA	lied current	Rated current (Refer to Page 2)					
				Time	500+24/-0 h					



Tape & Reel Packaging Dimensions:

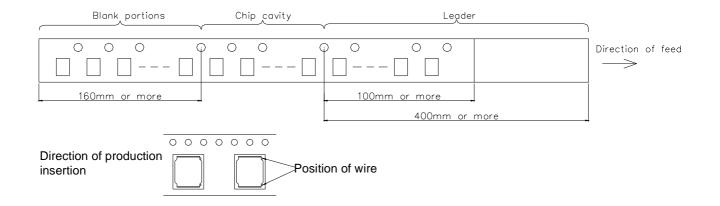
Dimensions Unit : mm





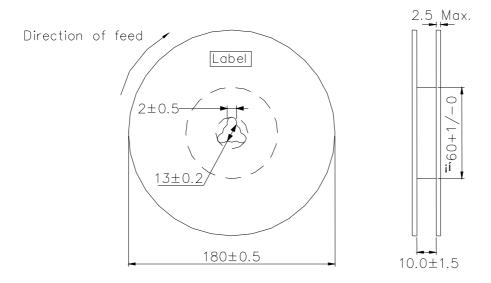
Ao	Bo	W	F	E	P1	P ₂	P٥	D ₀	Т	K
3.2 ±0.1	3.2 ±0.1	8.0 ±0.2	3.5 ±0.1	1.75 ±0.1	4.0 ±0.1	2.0 ±0.05	4.0 ±0.1	Ф1.5 +0.1 -0	0.3 ±0.05	1.4 ±0.1

Direction of rolling



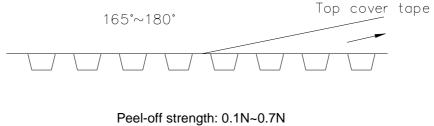


Reel



Label position:on the opposite sie of sprocket holes side of reel

Top tape strength



Peel-off angle:165°~180° Peel-off speed: 300mm/mm

Quantity per reel : 2K pcs